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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

S. Wu

Application No.: 09/666,902

Filed: September 20, 2000

For: **PROCESSOR ALLOCATION FOR CHANNELS IN A VIDEO MULTI-PROCESSOR SYSTEM**

Commissioner for Patents
Washington, D.C. 20231

| CERTIFICATE OF MAILING | |
|--|-----------------------|
| I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail, postage prepaid, in an envelope addressed to Commissioner for Patents, Washington, D.C. 20231 on January 8, 2001 | |
| By: | <u>Michele Hollis</u> |
| | Michele Hollis |

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

In accordance with 37 C.F.R. §1.56, Applicants wish to call the attention of the Examiner to the following references:

U. S. PATENT(S) :

| <u>Patent No.</u> | <u>Patentee</u> | <u>Date of Issue</u> |
|-------------------|------------------|----------------------|
| 5,623,312 | Yan et al. | Apr. 22, 1997 |
| 5,650,860 | Uz | Jul. 22, 1997 |
| 5,694,170 | Tiwari et al. | Dec. 02, 1997 |
| 5,701,160 | Kimura et al. | Dec. 23, 1997 |
| 5,719,986 | Kato et al. | Feb. 17, 1998 |
| 5,949,490 | Borgwardt et al. | Sep. 07, 1999 |

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FOREIGN PATENT DOCUMENT(S) :

| <u>Country</u> | <u>No.</u> | <u>Date of Publication</u> |
|----------------|--------------|----------------------------|
| International | WO 00/21302 | Apr. 13, 2000 |
| European | EP 0 851 656 | Jul. 01, 1998 |

PUBLICATION(S) :

G. Keesman, et al., "Bit-rate control for MPEG encoders," Signal Processing: IMAGE Communication, vol. 6, pp. 545-560, 1995.

D. Bagni, et al., "Efficient Intra-frame Encoding and improved Rate Control in H.263 compatible format," NTG FACHBERICHTE, pp. 767-774 XP002095679 ISSN: 0341-0196, September 10, 1997.

Björk, Niklas et al., "TRANSCODER ARCHITECTURES FOR VIDEO CODING," IEEE Transactions on Consumer Electronics, Vol. 44, No. 1, February 1998, pp. 88-98.

Staff of Cable Television Laboratories Inc., "DIGITAL TV SOLUTIONS," From the Labs: Cable World, February 1, 1999.

Gersho, Allen et al., "Vector Quantization and Signal Compression," 1991 Kluwer Academic Press, Chapter 8. Bit Allocation and Transform Coding, Demand Assignment Integer-Constrained Allocation Algorithm, pgs. 230-231.

Applicants also wish to bring to the Examiner's attention to the following co-pending U.S. patent applications:

| <u>Application No.</u> | <u>Filing Date</u> |
|------------------------|--------------------|
| /09/665,372 | September 20, 2000 |
| /09/667,734 | September 22, 2000 |
| /09/669,517 | September 25, 2000 |

Consideration of these references by the Examiner is respectfully requested. To date, a first Office Action on the merits has not been received in connection with this application.

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This Information Disclosure Statement is therefore believed to be timely filed.

The citation of these references does not constitute an admission that they are relevant or material to the claims of the present invention.

Copies of the above-listed references are enclosed, together with Form PTO-1449.

Respectfully submitted,



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Date: January 8, 2001
ATTORNEY DOCKET NO.: GIC-607